Surname	Centre Number	Candidate Number
Other Names		0



GCSE - NEW

C500U10-1





# COMPUTER SCIENCE – Component 1 Understanding Computer Science

MONDAY, 14 MAY 2018 - MORNING

1 hour 45 minutes

For Examiner's use only					
Question	Maximum Mark	Mark Awarded			
1.	11				
2.	18				
3.	7				
4.	11				
5.	6				
6.	10				
7.	8				
8.	9				
9.	8				
10.	12				
Total	100				

#### **INSTRUCTIONS TO CANDIDATES**

Use black ink or black ball-point pen.

Write your name, centre number and candidate number in the spaces at the top of this page.

Answer all questions.

Write your answers in the spaces provided in this booklet.

If you run out of space, use the continuation pages at the back of the booklet, taking care to number the question(s) correctly.

### **INFORMATION FOR CANDIDATES**

The number of marks is given in brackets at the end of each question or part-question.

You are reminded of the necessity for good English and orderly presentation in your answers.

The use of calculators is not permitted in this examination.

The total number of marks is 100.

Questions 1, 2, 3 and 6 will require you to draw on knowledge from multiple areas of your course of study.

1.	Sound	and	graphics	can	be	stored	by	computer	systems.
----	-------	-----	----------	-----	----	--------	----	----------	----------

(a)	Tick (/) the boxes	s below to	show	whether	the	statements	about	sound	sampling	are
	TRUE or FALSE.									[3]

	STATEMENT	TRUE	FALSE	
	Natural sound is in digital form. This is sampled and converted into analogue form to be stored by computer systems.			
	A sound sample rate of 16 KHz means the wave is sampled 160,000 times a second.			
	The lower the sampling rate, the better the quality of the sound file.			
	State what is meant by metadata and give an exa iles.	imple of me	tadata stored	d in graphics [2]
	Kevin has compressed a sound file to reduce its si converted the resulting file to a lossless file format			ion. He then
E	Explain the disadvantages of this process.			[3]
•••••				
•••••				

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- (d) A simple bitmap graphic has the following characteristics:
  - 8-bit colour depth 10 × 300 pixels.

	e size of this graph		[3]
***********	 	 	 
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The and a	computer systems used at a warehouse have a single-core Central Processing Unit (CF a typical Von Neumann architecture.	U
(a)	Describe the role of the following components.	[2
	Control Unit	
	Arithmetic Logic Unit	[1
(b)	Describe RISC type processors.	[2

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(c)	Explai	n how r	main memory is used dur	ring the fetch-decode-exec	cute cycle. [3]
•••••					
(d)	The co	omputer	r systems used at the wa	rehouse are starting to ru	n slowly when searching
	(i)	The wa	rehouse is considering ref	eplacing the CPUs in thei	r computer systems with
			CPU 1	CPU 2	
		(	CPU 1 3 GHz Quad-core 4MB cache	CPU 2  4 GHz Dual-core 8MB cache	
	(	2	3 GHz Quad-core	4 GHz Dual-core 8MB cache	[6]
		2	3 GHz Quad-core 4MB cache	4 GHz Dual-core 8MB cache	[6]
		2	3 GHz Quad-core 4MB cache	4 GHz Dual-core 8MB cache	[6]
		2	3 GHz Quad-core 4MB cache	4 GHz Dual-core 8MB cache	[6]
		2	3 GHz Quad-core 4MB cache	4 GHz Dual-core 8MB cache	[6]
		2	3 GHz Quad-core 4MB cache	4 GHz Dual-core 8MB cache	[6]
		2	3 GHz Quad-core 4MB cache	4 GHz Dual-core 8MB cache	[6]

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(ii)	The warehouse staff could use different types of utility software to improve the disk access speed.	
	Give examples of utility software that could be used to improve disk access speed. [4]	

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1-1		[0]
(a)	Describe what is meant by the term embedded systems.	[2]
•••••		••••••
•••••		
(b)	A heater is controlled by an embedded system.	
	<ul> <li>There is a power button (A) to turn the power going to the heater on or off.</li> <li>A temperature sensor (B) will turn the heater on when the temperature is be 20°C, provided the power button has been left on.</li> <li>A manual override switch (C) will turn the heater on, regardless of the temperature provided the power button has been left on.</li> </ul>	
	Construct a logic statement to represent this situation, using the symbols A, B, and	C. [3]
(c)	Bit patterns can be used to represent the different states of an embedded system. These bits can be manipulated by several different operations.	
	Perform an arithmetic shift left by 3 places on the 16 bit binary number	

	cols provide an agreed set of rules to allow networked devices to communicate.	Examir only
(a)	<b>Tick</b> (✓) the box to identify the protocol used to <b>send</b> email messages to an email server. [1]	
	SMTP	
	FTP	
	POP3	
	IMAP	
(b)	Describe the role of the Ethernet protocol. [2]	
•••••		
•••••		
•••••		
(c)	Explain the difference between the HTTP and HTTPS protocols. [2]	
•••••		

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Describe each of the following layers in the TCP/IP 5-layer model for data transmission:

(d)

[6]

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<ul> <li>Physical layer</li> <li>Transport layer</li> <li>Application layer</li> </ul>	
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**5.** Describe and give examples of the following types of programming errors.

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[6]

	Error	Description	Example
	Runtime/ execution		
	Linking		
	Rounding		
6.		of programming language are used by programming languages	

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(b) The following code, which contains errors, is written in a certain high-level programming language:

```
1
     Declare CountProc
2
     Count is integer
3
     set Count = 0
4
5
     for i = 1 t 1000
          Cout = Count + i
б
7
          output Count
8
    next i
9
     End Subroutine
10
```

(i)	Describe the difference in the way in which this code would be interpreted and way in which this code would be compiled.	I the [2]
(ii)	Discuss the benefits and drawbacks of compilers and interpreters.	[4]
••••••		•••••
•••••		

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# **7.** The table below shows data for a payroll system.

Field	Example Data	Further information
Surname	Williams	
National Insurance (NI) number	JN126523A	
Job title	Semi-skilled	Apprentice, semi-skilled, skilled, supervisor
Week number	12	
Full time	Υ	Y or N
Hours worked (current week)	35	Maximum 40 hours per week Full hours only
Hourly pay rate	7.50	Maximum of 15.00 per hour

July pay	y rate	7.50	Maximum of 15.00 per floar	
			this data, justifying your choice.	[2]
(b)	Some of the data can be	validated. Design <b>thr</b>	ee different types of validation check.	
Valida	tion Check 1			[2]
Field:				
Type o	of check:			
Rule:				
	tion Check 2			[2]
Type o	of check:			
Rule:				
•••••				

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	ation C	heck 3						[2]	Exami only
	of chec	:k:							
(a)	Comple	ete the table	below, co	onverting b	etween de	enary, bina	ry and	hexadecimal [3]	
		Denary		Binary	Н	exadecima	I		
		41 <sub>10</sub>		00101001 <sub>2</sub>		29 <sub>16</sub>			
		58 <sub>10</sub>				3A <sub>16</sub>			
				10101111 <sub>2</sub>		AF <sub>16</sub>			
		253 <sub>10</sub>		11111101 <sub>2</sub>					
(b)	Comple	ete the table to	calculate th	ne binary ad	ldition of 14	4 <sub>10</sub> to 67 <sub>10</sub> u	sing an 8	-bit register. [3]	
4 <sub>10</sub>									

(c)	Using a suitable example, explain the concept of overflow in relation to binary addition	Exam onl [3]
************		
•••••		
•••••		
•••••		
(a)	Complete the following truth table.	[4]

(a) Complete the following truth table. 9.

P	Q	P+Q	P.Q	P.Q	$\overline{P.Q} + (P+Q)$
1	1				
1	0				
0	1				
0	0				

Draw a truth table for the expression: (b)

$$X = A.B + \overline{A.B}$$

iner

[4]

Exa	m	in	е
0	nl	V	

vuine	ties that must be identified and protected against.	nt inherent technical			
(a)	Describe the following forms of attack on cybersecurity:				
	(i)	SQL injection.	[2		
	(ii)	IP address spoofing.	[2		
	<b></b>				
(b)	Des	cribe methods of identifying these vulnerabilities.	8]		
(b)	Des	cribe methods of identifying these vulnerabilities.	3]		
		cribe methods of identifying these vulnerabilities.			

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## **END OF PAPER**

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